

HTH8G06P1K4H(B) 1400W, 1.8 - 230 MHz LDMOS Amplifier

Product datasheet

Description

The HTH8G06P1K4H(B) is high ruggedness device designed for use in high VSWR ISM, Broadcast and Mobile Radio applications. Their unmatched Input/Output design supports frequency use from 1.8 to 230 MHz.

Features

- Saturated output power > 1400W
- Operating Drain Voltage: 50V
- Efficiency > 70%
- Device can be used on a single-ended or in a push-pull configuration
- Integrated ESD protection
- Excellent thermal stability due to low thermal resistance package
- Enhanced robustness design without device degradation

Applications

- Industrial Scientific Medical (ISM)
 - Laser generation
 - Plasma generation
 - Particle accelerators
 - o MRI, RF ablation and skin treatment
 - Industrial heating, welding and drying systems

Ordering Information

Part Number	Description	
HTH8G06P1K4H(B)	Tray Package	
HTH8G06P1K4H(B) EVB	108 MHz EVB	



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Typical Performance

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RF Characteristics (Pulsed-CW)

Vdd (V)	Gain (dB)	Pout(dBm)	Pout(W)	Eff (%)
50	22.9	61.5	1412	73.1

Test conditions unless otherwise noted: 25 °C,Freq@108 MHz, IDQ=100mA, PW=100us, DC=10% test on WATECH Application Board

Absolute Maximum Ratings

Parameter	Range/Value	Unit
Drain voltage (VDSS)	-0.5 to +115	V
Gate voltage (V _{GS})	-5 to +10	V
Storage Temperature (Tstg)	-55 to +150	°C
Junction Temperature (T _J)	-40 to +225	°C

Electrical Specification

DC Characteristics (Way A/B)

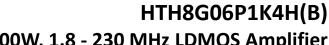
Parameter	Conditions	Min	Тур	Max	Unit
Breakdown Voltage V(BR)DSS	Vgs=0V, Ids=255uA	115	-	-	V
Gate-Source Threshold	\/dc=10\/ dc=2FF\	1.0	2.2	2.0	V
Voltage V _{GS(th)}	Vds=10V, Ids=255uA	1.8	2.3	2.8	V
Drain Leakage Current loss	Vds=50V, Vgs=0V	-	-	0.5	uA
Gate Leakage Current IGSS	Vds=0V, Vgs=5V	-	-	0.5	uA

Load Mismatch Test

Condition	Test Result
VSWR=65:1 at all Phase Angles, VDD=+50Vdc, IDQ = 100 mA,	
@Freq = 108MHz, PW = 100 us, DC = 10%,	No Device Degradation
Pout = 1400W test on WATECH Application Board	

Thermal Information

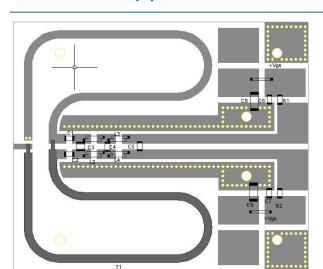
Parameter	Condition	Value (Typ)	Unit
Thermal Resistance	Ti- 60°C massured under DC condition	0.12	°C /W
Junction to Case (Rтн)	Tj= 60°C, measured under DC condition	0.13	C/W

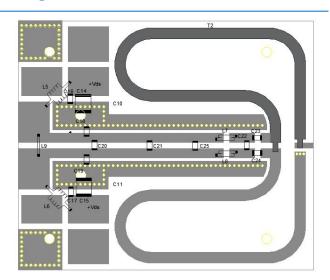




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HTH8G06P1K4H(B) 108 MHz Reference Design





EVB Layout @108 MHz

Bill of Materials (BoM) - HTH8G06P1K4H(B) 108 MHz Reference Design

Reference	Value	Description	Manufac turer	P/N
		1400W, 1.8-230		
Q1	-	MHz LDMOS	Watech	HTH8G06P1K4H(B)
		Power Transistor		
C1, C2, C6, C7, C12, C13	1000pF	MLCC	Murata	GRM21A5C2E680FW01
C3	47pF	MLCC	Dalicap	DLC70B470JW501XT
C4	39pF	MLCC	Dalicap	DLC70B390JW501XT
C5	200pF	MLCC	Dalicap	DLC70B201JW301XT
C8, C9, C10, C11	4.7uF	MLCC	YAGEO	CC1210KKX5R9BB475
C14, C15	150pF	MLCC	Dalicap	DLC70B151JW301XT
C16,C17	82pF	MLCC	Dalicap	DLC70B820JW501XT
C18	10pF	MLCC	Dalicap	DLC70B100JW501XT
C19	56pF	MLCC	Dalicap	DLC70B560JW501XT
C20, C21	820pF	MLCC	Dalicap	DLC70B821JW301XT
L1, L2, L3, L4	D = 6 mm, length = 1.6 mm	1.5 turn 0.8 mm copper wire	-	-
L5, L6	D = 3.0 mm, length = 4 mm	5.0 turn 0.8 mm copper wire	-	-
L7, L8	D = 4.0 mm,	2.5 turn 0.8 mm	-	<u>-</u>

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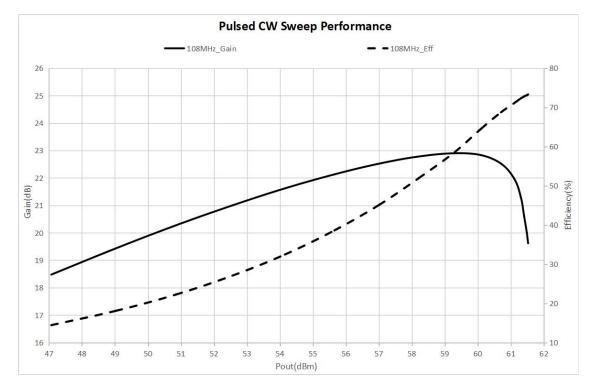




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	length = 2.4 mm	copper wire		
L9	R:1.5,W:13,H:4.8	Enameled wire	-	-
Т1	25Ω,length =	comi rigid cooy		
T1	160 mm	semi rigid coax		
T2	25Ω,length =	anni vinid annu		
T2	160 mm	semi rigid coax		
PCB	RF35 (er = 3.5), 30 mil (0.762 mm), 35 μm (1oz)			

Performance Plots HTH8G06P1K4H(B) 108 MHz



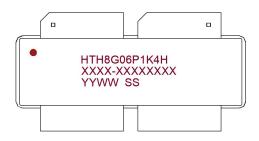
Pulsed CW, Gain and Eff vs Pout

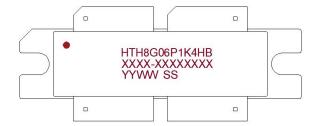
Test conditions unless otherwise noted: 25 °C, VDD = +50Vdc, IDQ = 100 mA, PW = 100us, DC = 10% tested on WATECH Application Board

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Package Marking and Dimensions

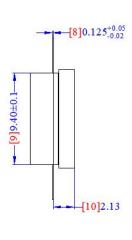




- Line1 (fixed): Device name in work order
- Line2 (unfixed): Mark Lot number in work order (Sample: E596-20140001)
- Line3 (unfixed): Date Code + "SS"(The last two digits of sub lot Number)

This Marking SPEC only stipulates the content of Marking. For marking requirements such as font and size, please refer to the latest version of "Watech Product Printing Specification"

700750 [1] 32.26 [2] 25.40 [3] 11.68 [7] 13.72 [11] 31.24±0.3



Remark: 1.Unit: mm; 2.Unlabeled tolerance is ± 0.13 mm.

ACC3210S-4L; Earless Flanged Balanced Air Cavity Ceramic Package; 4 Leads

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WITELH Product datasheet [1]41.15 [2]35.56 [4]25.40 [12]2.16 [5]11.68-[8]3.23±0.07---[14]9.40±0.10-[6]16.62±0.5 -[7]10.16- $[11]4.52\pm0.30$ [15]0.125±0.03 -[13]1.62±0.07 -[10]13.72-[3]31.24±0.30

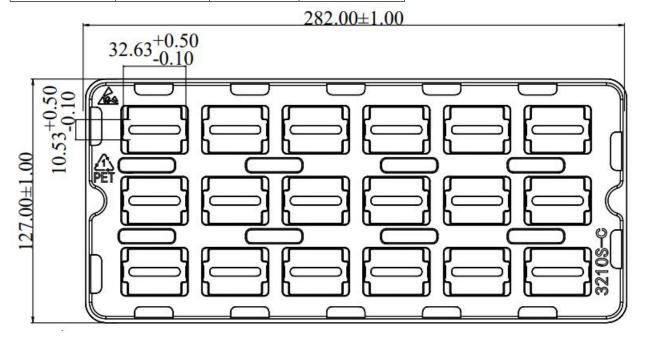
Remark: 1.Unit: mm; 2.Unlabeled tolerance is ± 0.13 mm.

ACC3210B-4L; Flanged Balanced Air Cavity Ceramic Package; 2 Mounting holes, 4 Leads **Package Dimensions**

Packaging Information

HTH8G06P1K4H:

Package Type	Qty/Tray(pcs)	Qty/Box(pcs)	Qty/Carton(pcs)
ACC3210S-4L	18	90	540



Packaging Descriptions

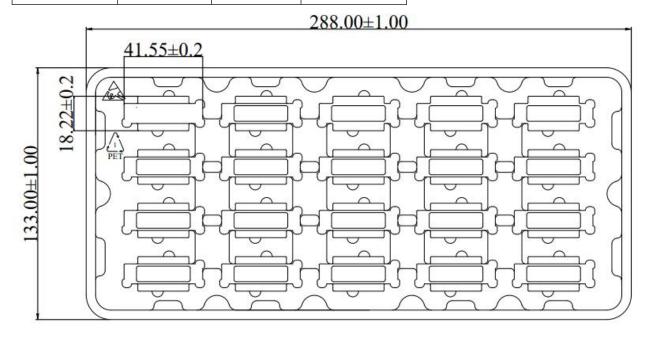
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Package Type Qty/Tray(pcs) Qty/Box(pcs) Qty/Carton(pcs) ACC3210B-4L 20 100 600



Packaging Descriptions

Handling Precautions

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	Class 1B	JESD22-A114
ESD – Human Body Model (MM)	Class A	EIA/JESD22-A115
ESD – Charged Device Model (CDM)	Class III	JESD22-C101



RoHS Compliance

This product is compliant with the 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment), as amended by Directive 2015/863/EU.

Datasheet Status

Document status	Product status	Definition
Objective Datasheet	Design simulation	Product objective specification
Preliminary Datasheet	Customer sample	Engineering samples and first test results
Product Datasheet	Mass production	Final product specification



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Abbreviations

Acronym	Definition	
LDMOS	Laterally-Diffused Metal-Oxide Semiconductor	
CW	Continuous Waveform	
VSWR	Voltage Standing Wave Ratio	

Revision history

Document ID	Datasheet Status	Release Date	Revision Version
Rev 1.0	Preliminary	Sept. 2023	Preliminary
Rev 1.1	Product	May. 2024	Product version

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Contact Information

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For the latest specifications, additional product information, worldwide sales and distribution locations and information about WATECH:

• Web: <u>www.watechelectronics.com</u>

• Email: MKT@huatai-elec.com

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• Email: MKT@huatai-elec.com

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